

PAT-NO: JP362011941A  
DOCUMENT- JP 62011941 A  
IDENTIFIER:  
TITLE: MULTIPLEX PROCESSING CONTROL  
SYSTEM  
PUBN-DATE: January 20, 1987

## INVENTOR-INFORMATION:

NAME	COUNTRY
HAMANO, TAKEO	

## ASSIGNEE-INFORMATION:

NAME	COUNTRY
NEC CORP N/A	

APPL-NO: JP60151496  
APPL-DATE: July 10, 1985

INT-CL (IPC): G06F009/46

## ABSTRACT:

PURPOSE: To always secure a fixed CPU using time allocated to a specific process by dividing plural processes into several groups and distributing the CPU using time according to the ratios decided for each group.

Best Available Copy

Wte

CONSTITUTION: Each process uses a time slice control part 6 and a using CPU time integrating part 7 to receive a synchronously the allocation of the CPU using time and continue each processing after initialization. When a fixed period of time passed, an error calculating part 4 for CPU using time ratio is called out by a passed time control timer part 3. The part 4 first obtains the CPU using time of each process and processes each group for totalization of the CPU using time of each group. Then the CPU using time ratio is calculated for each group and an error is obtained to the set CPU allocation ratio. If the error absolute value exceeds a fixed level, a time slice interval changing part 5 is called out for adjustment of the CPU allocation. When the processing is through with all groups, the integrated CPU using time value for each process is set at 0. thus the processing is through.

COPYRIGHT: (C)1987,JPO&Japio

Best Available Copy